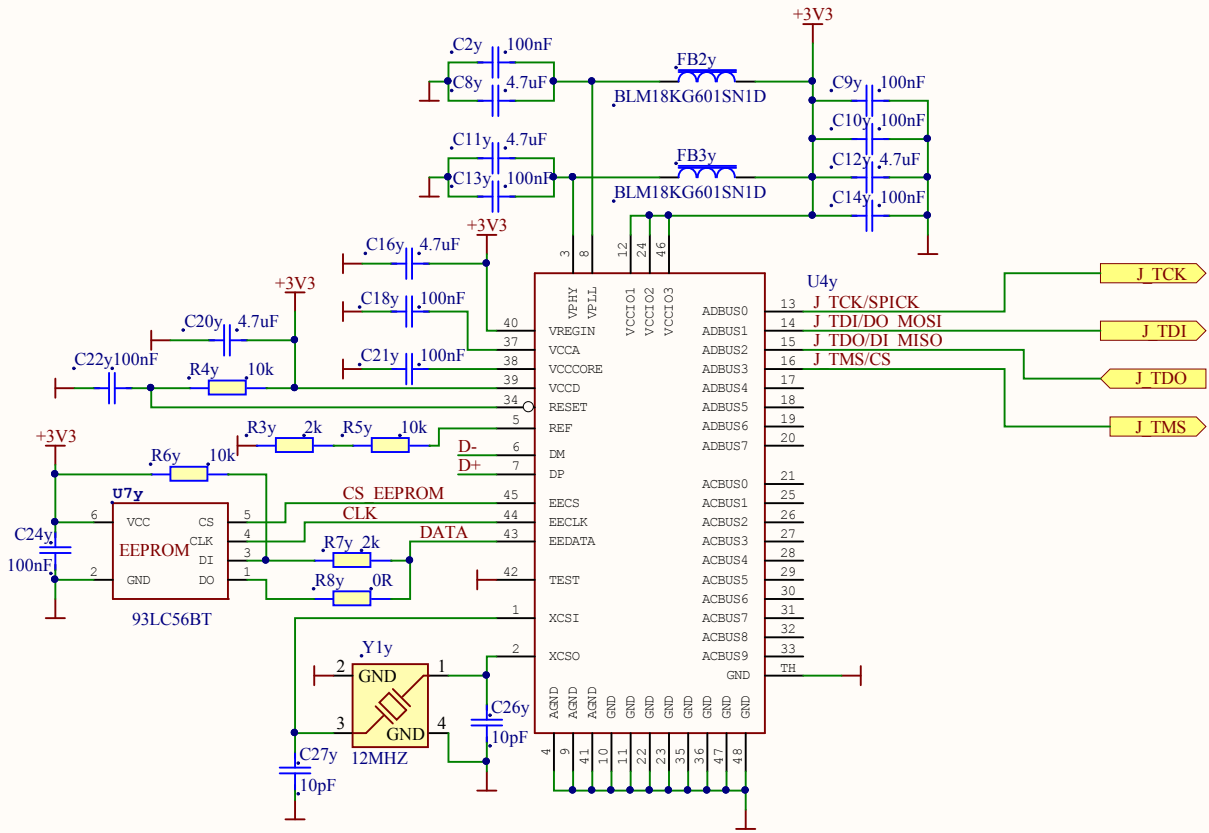
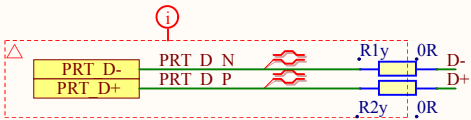
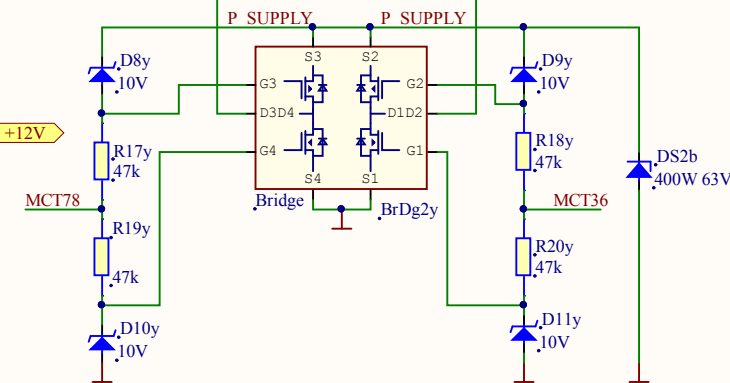
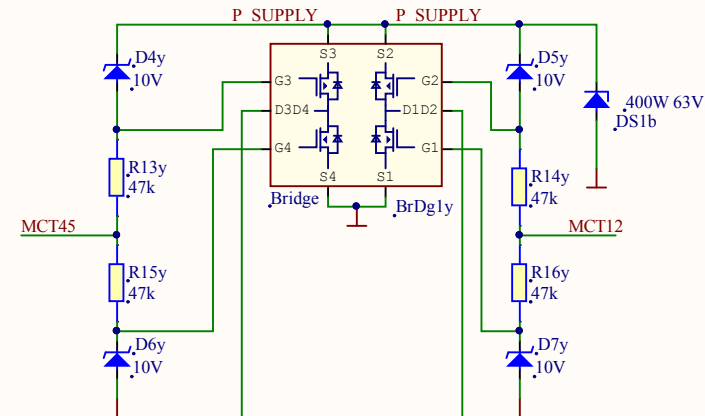
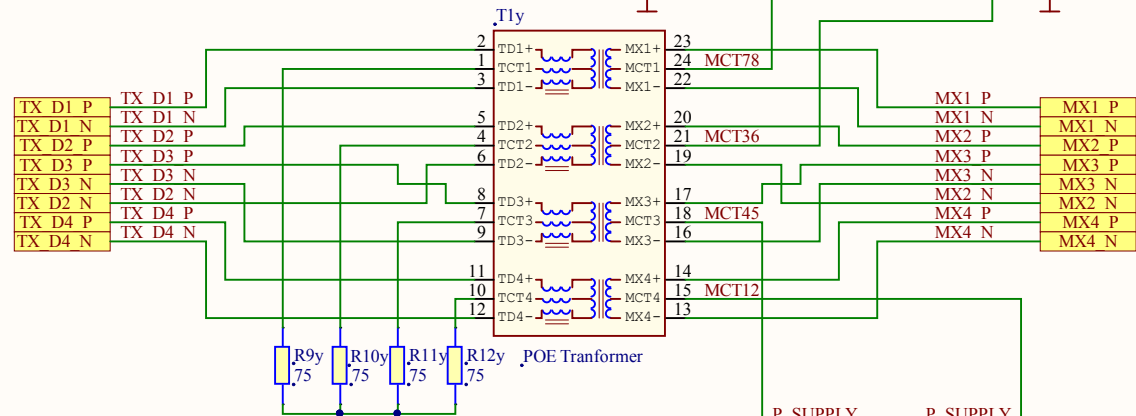
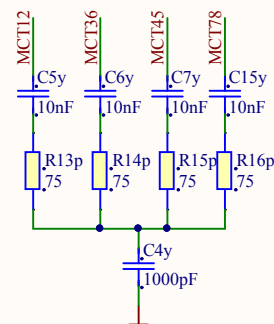
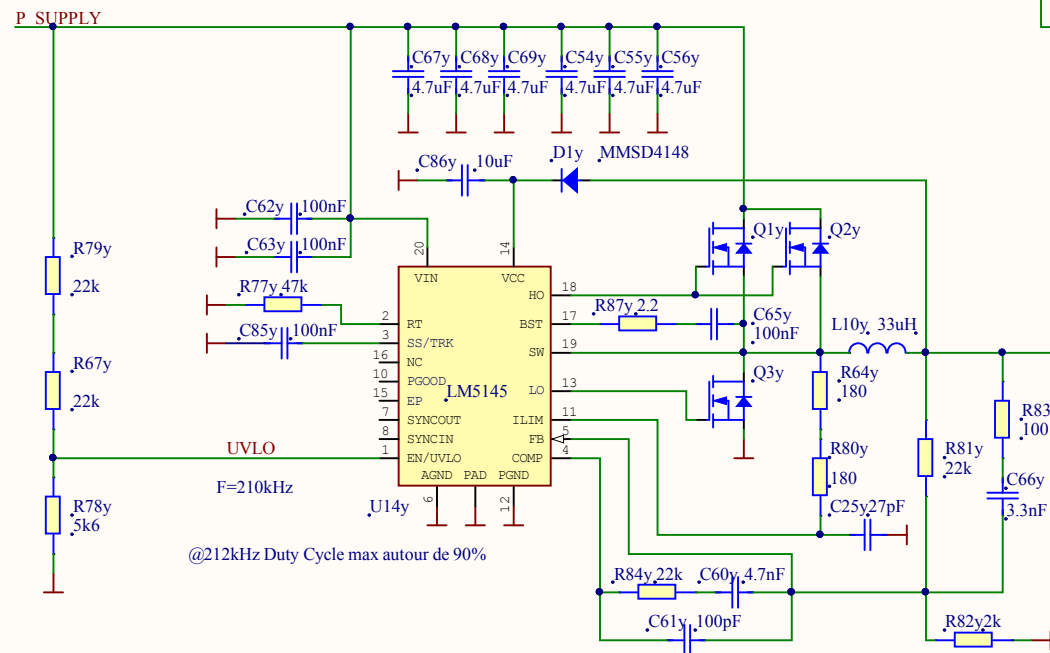
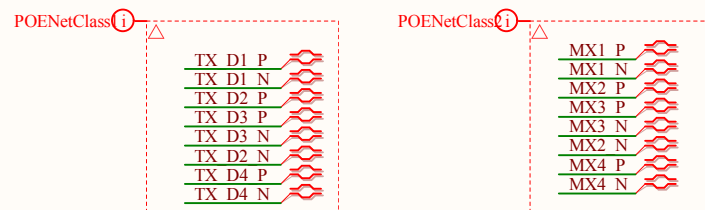
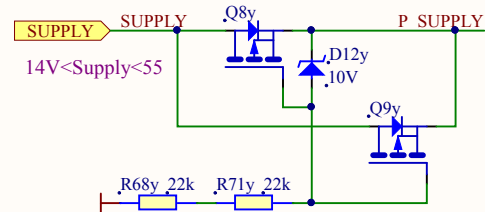


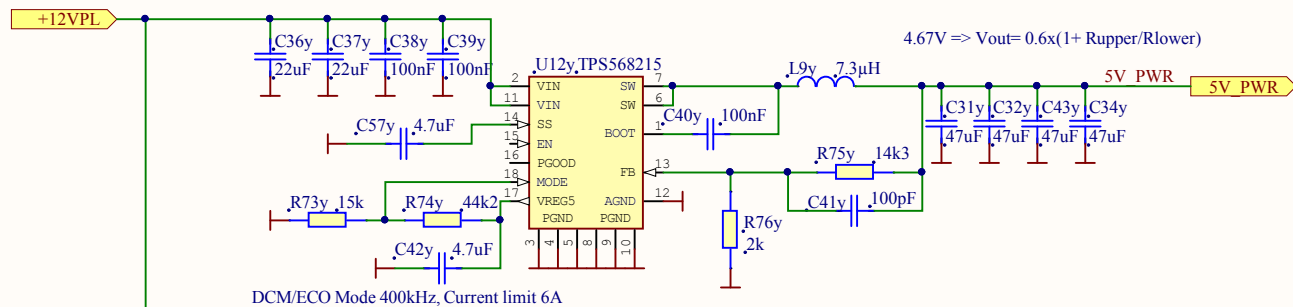
DiffPairNetClass



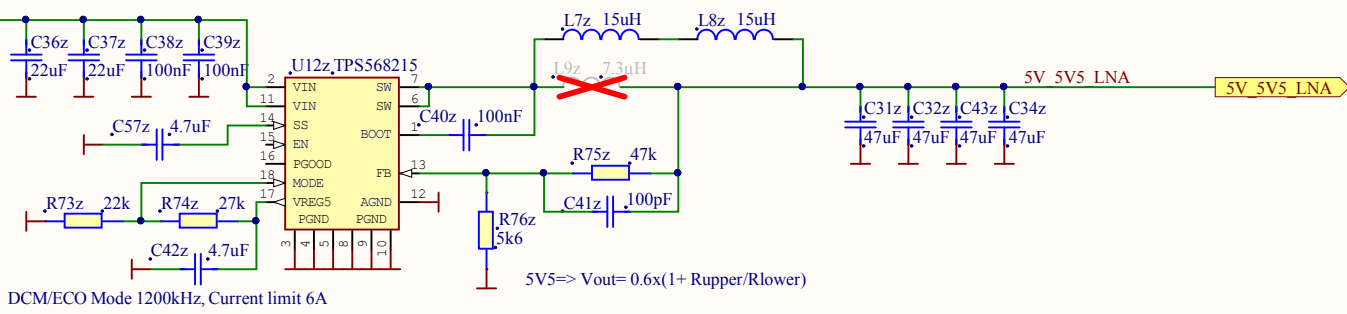
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A4		
Date:	22/07/2020	Sheet of
File:	V:\Work\...\CtrlProg.SchDoc	Drawn By:



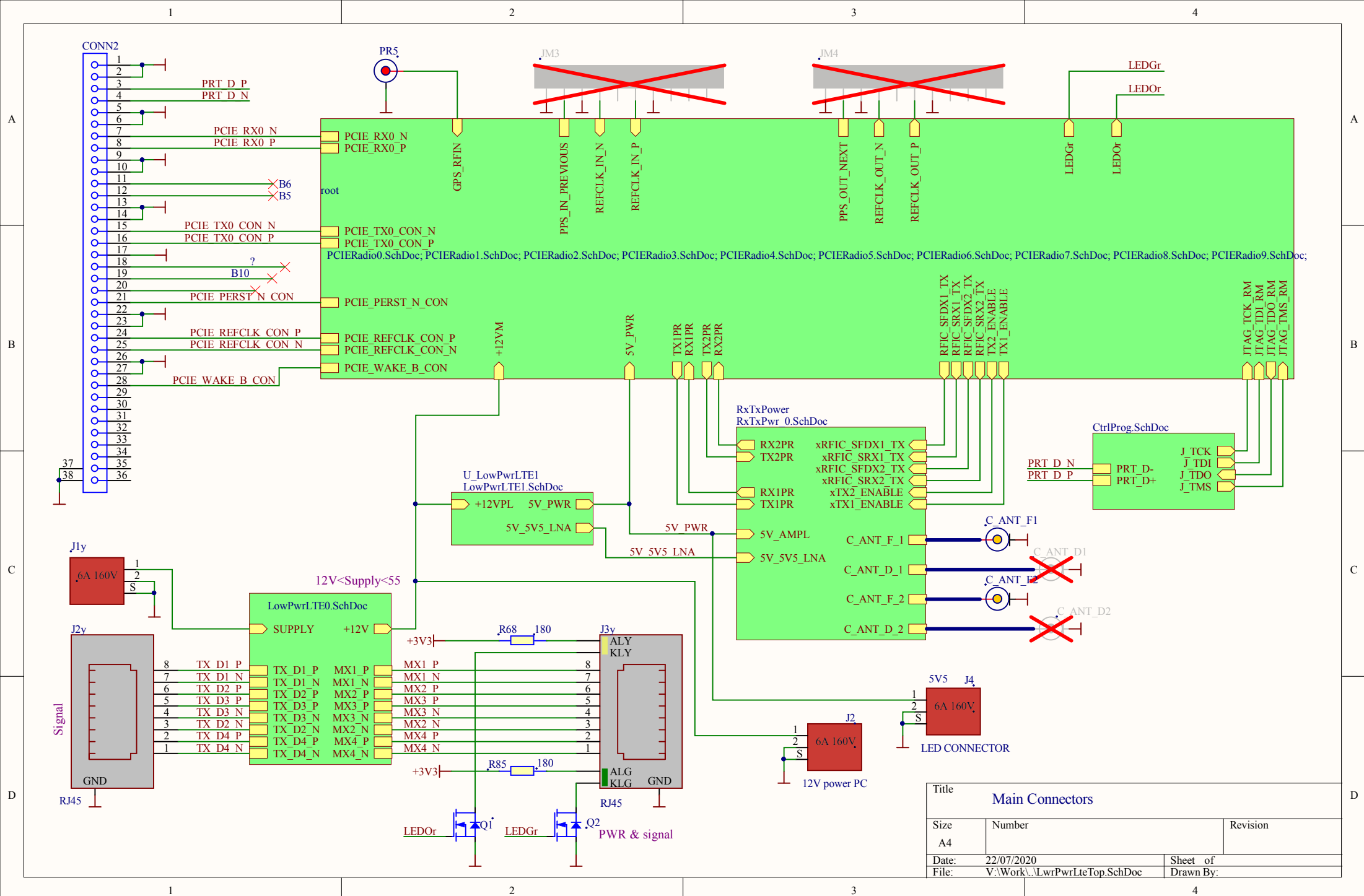
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Date: File:	22/07/2020 V:\Work\LowPwrLTE0.SchDoc	Sheet of Drawn By:

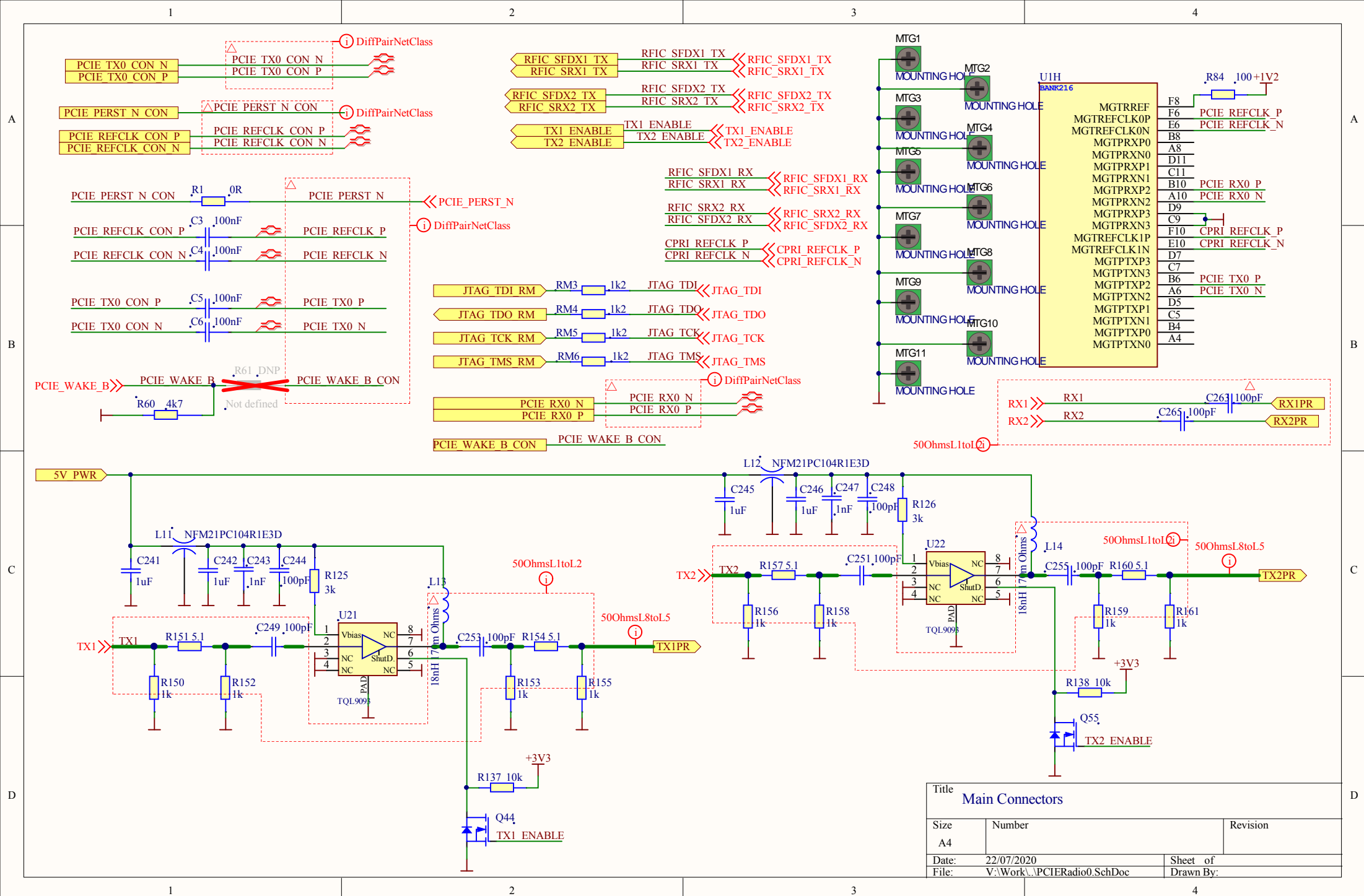


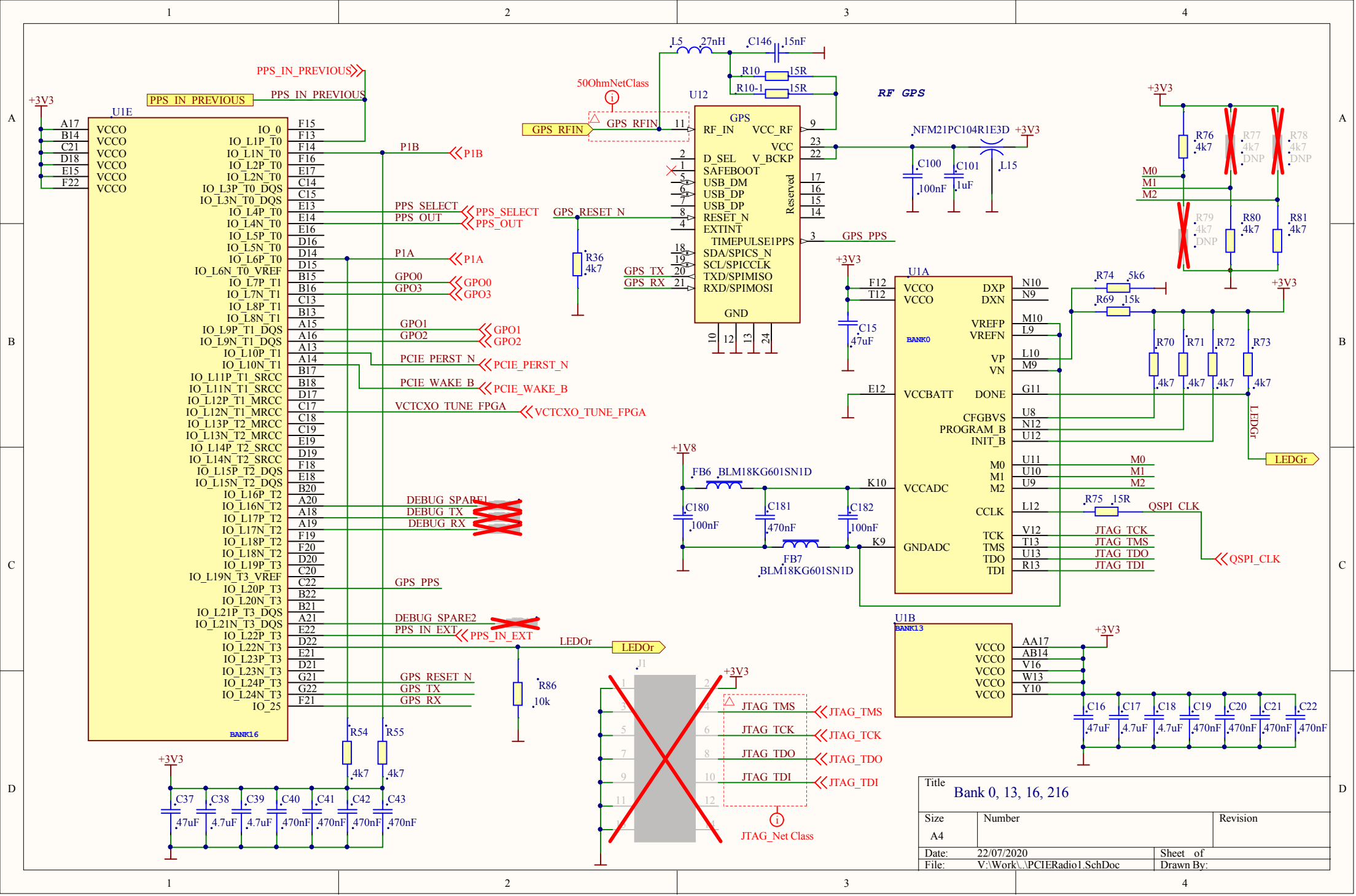
200mA Mettre plutôt un 22uH en cas de pB



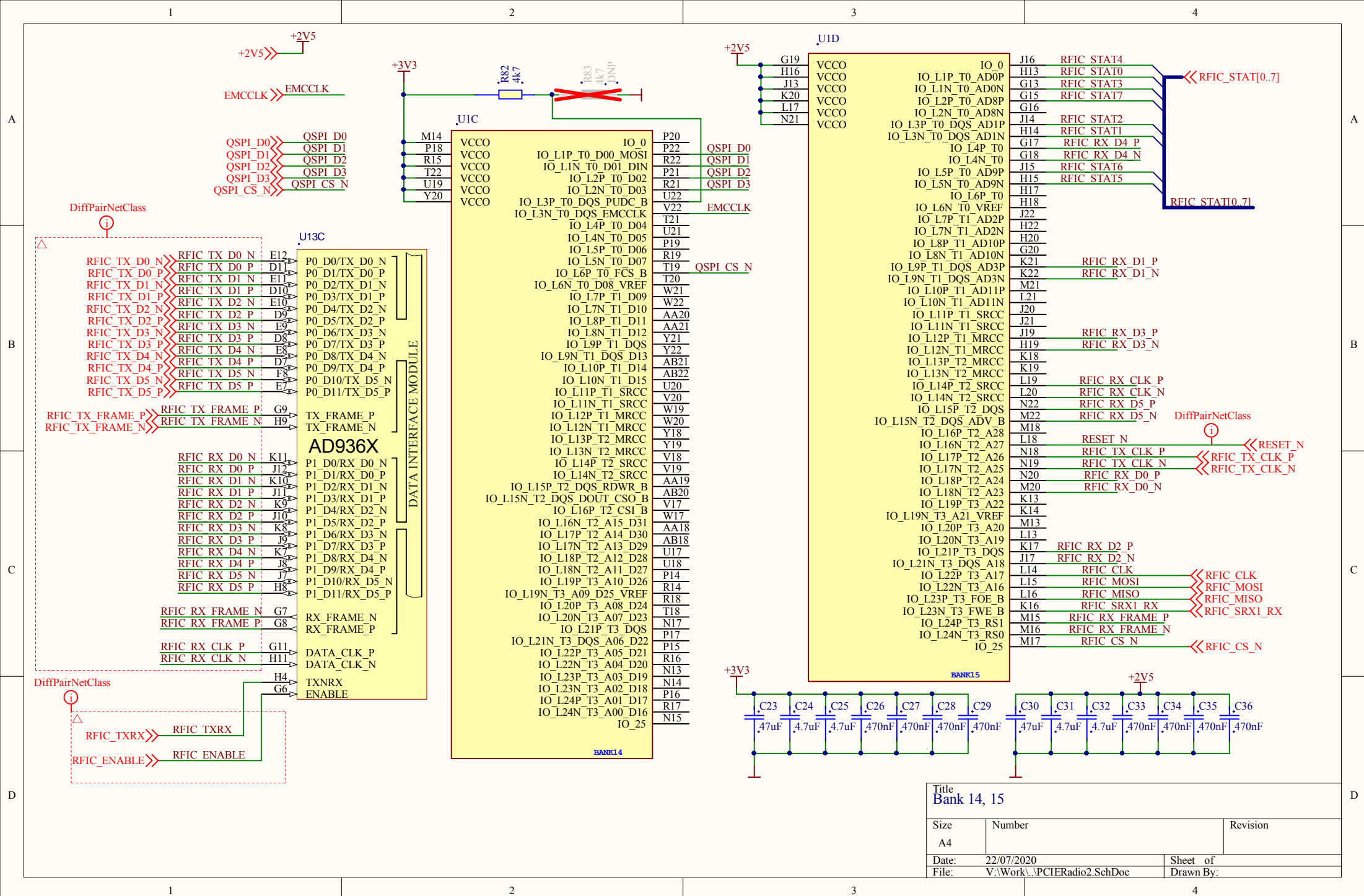
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Date:	22/07/2020	Sheet of
File:	V:\Work\LowPwrLTE1.SchDoc	Drawn By:

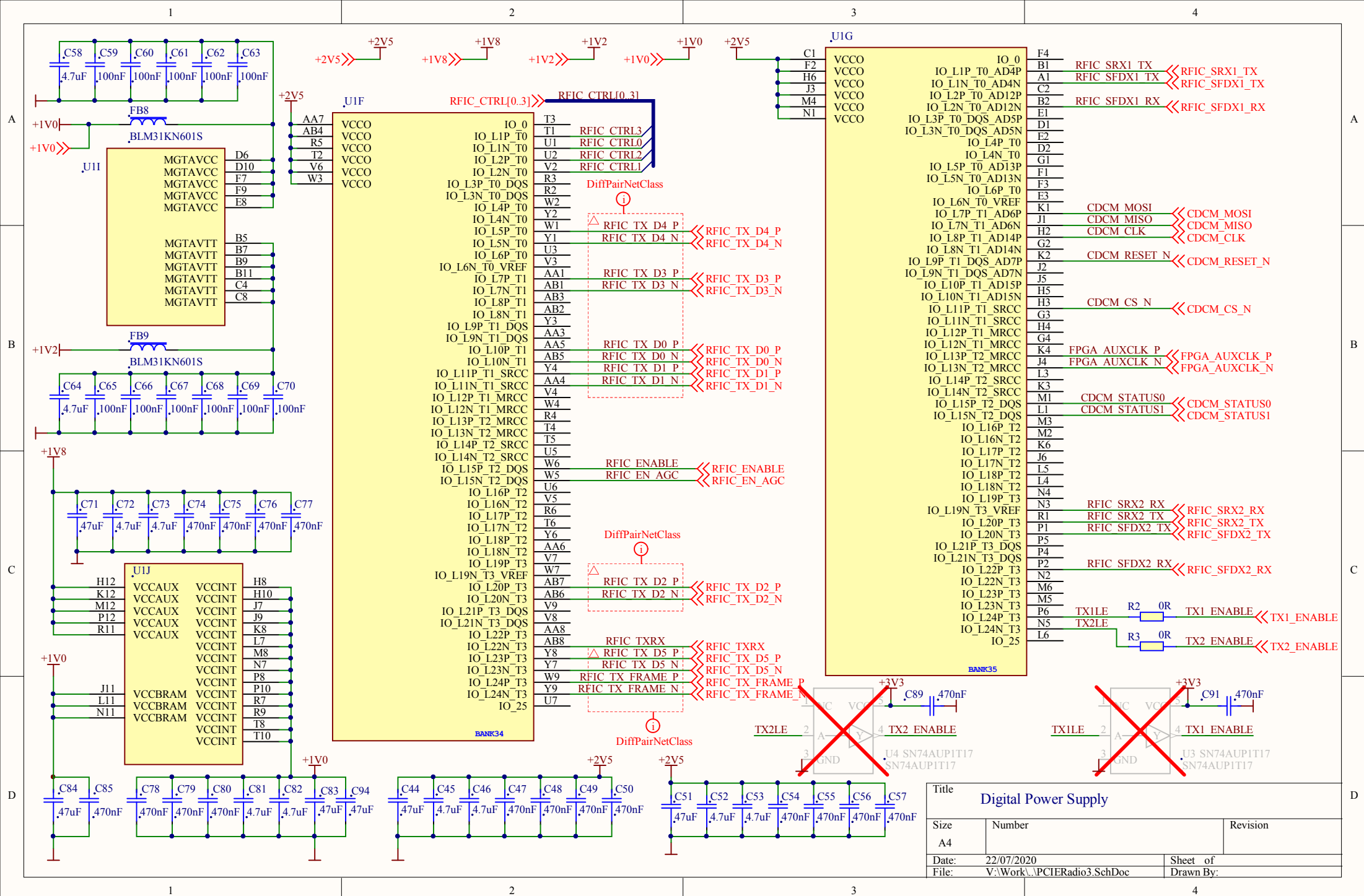




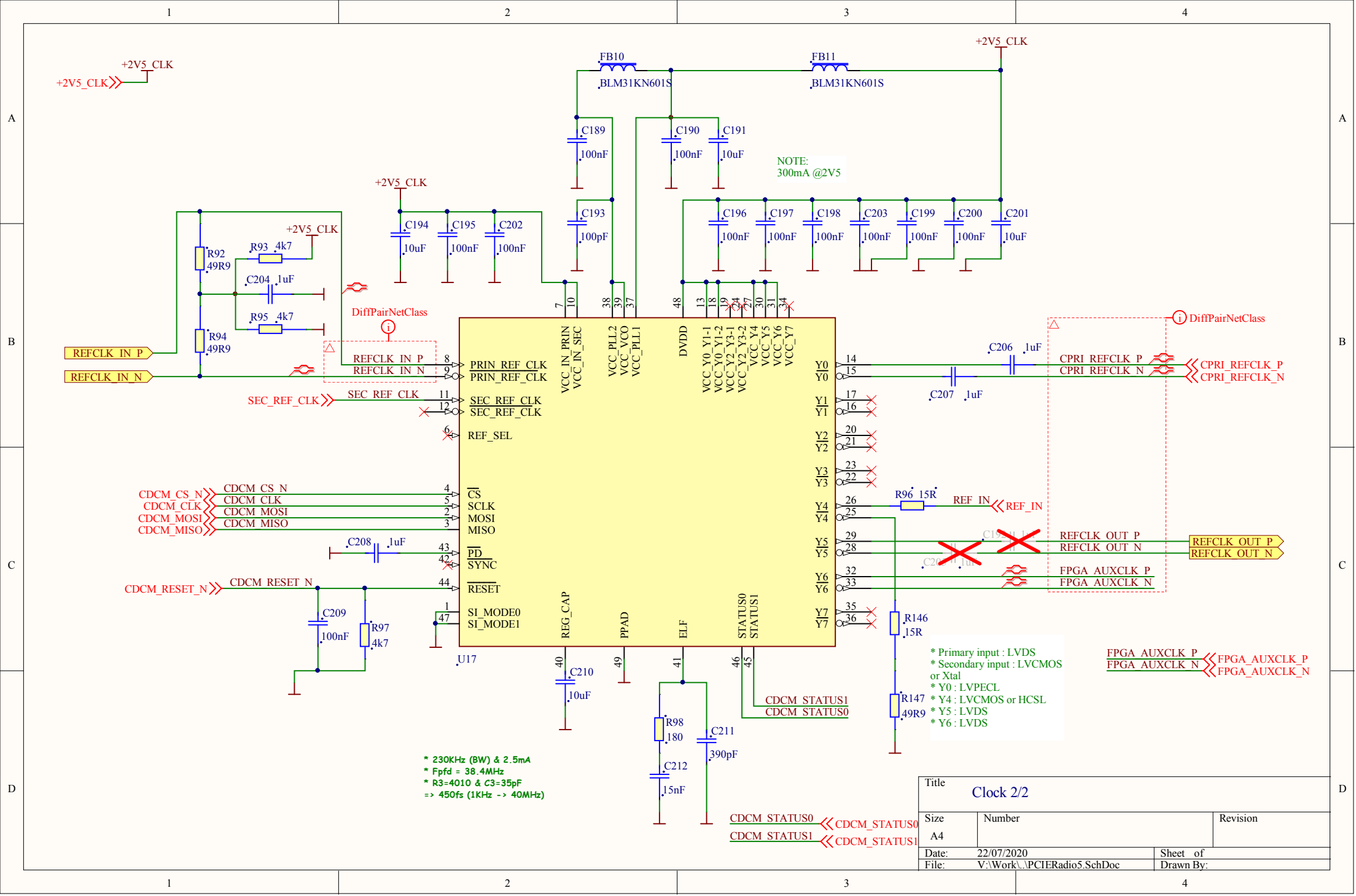




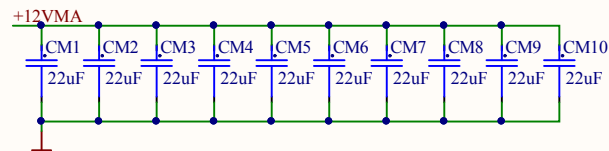
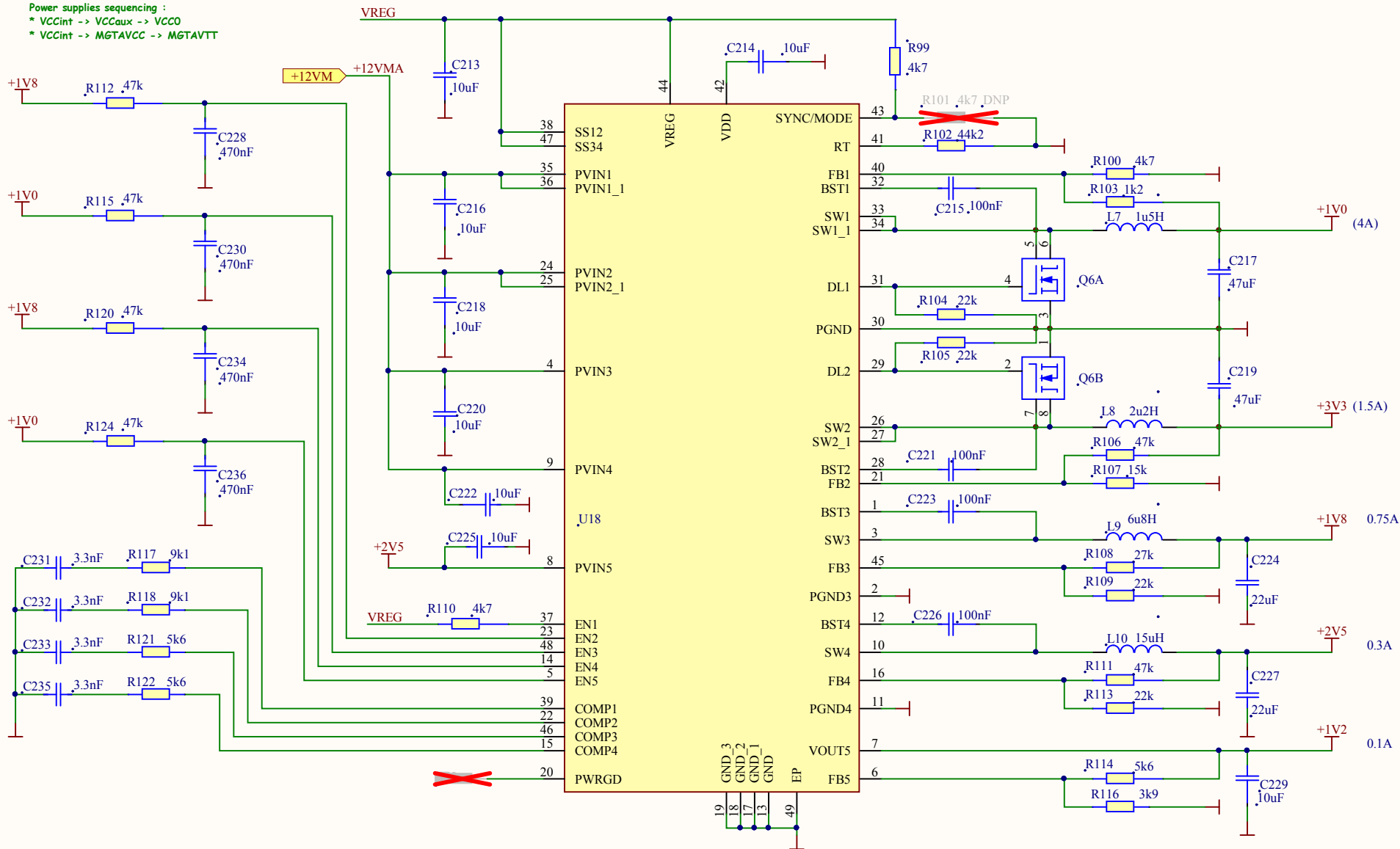








Power supplies sequencing :  
\* VCCint -> VCCaux -> VCCO  
\* VCCint -> MGTAVCC -> MGTAVTT



### Digital Power Supply

Title		
Size	Number	Revision
A4		
Date:	22/07/2020	Sheet of
File:	V:\Work\PCIERadio6.SchDoc	Drawn By:



A

B

C

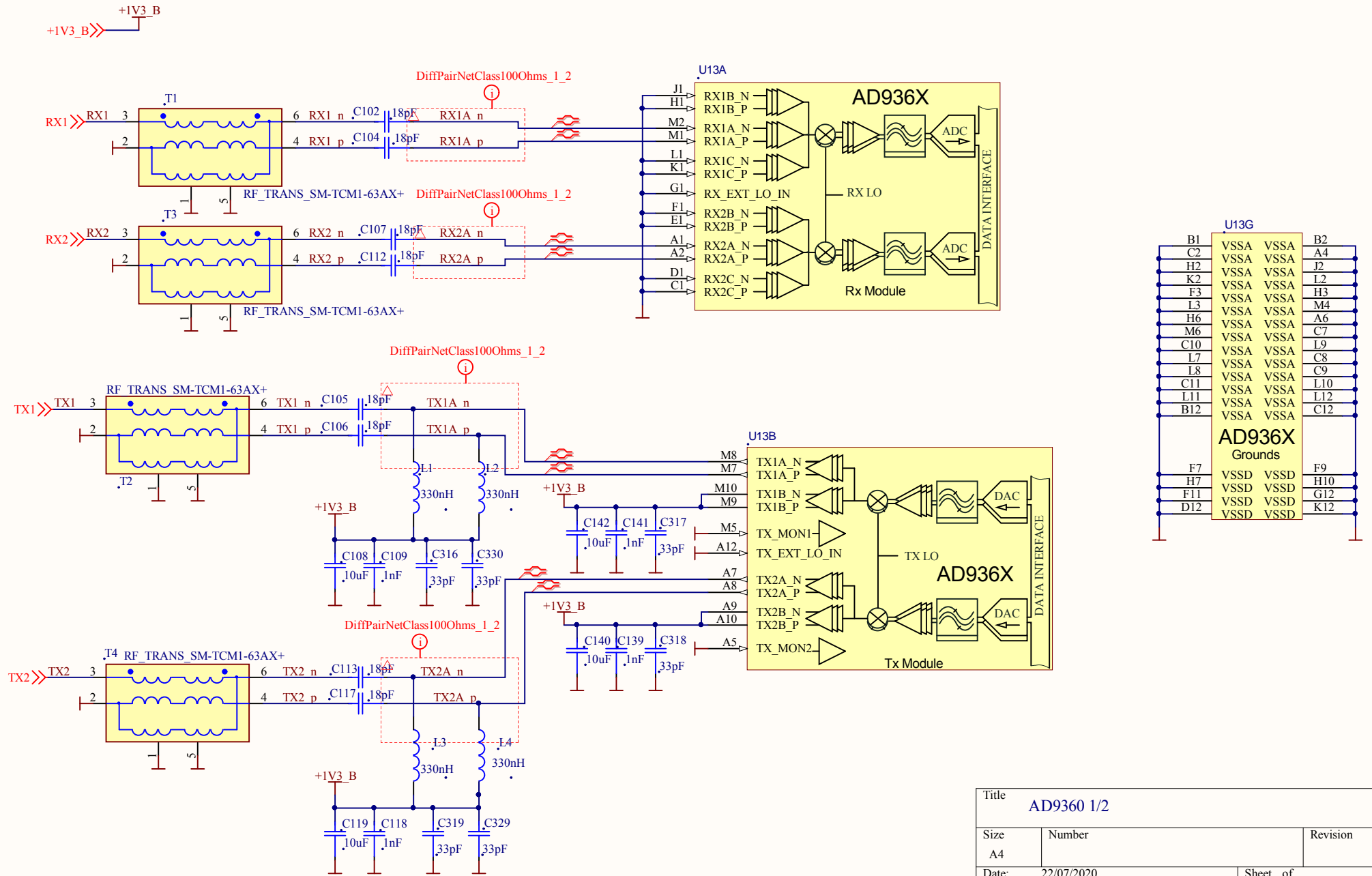
D

A

B

C

D



Title		
AD9360 1/2		
Size	Number	Revision
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Date:	22/07/2020	Sheet of
File:	V:\Work\PCIERadio8.SchDoc	Drawn By:





